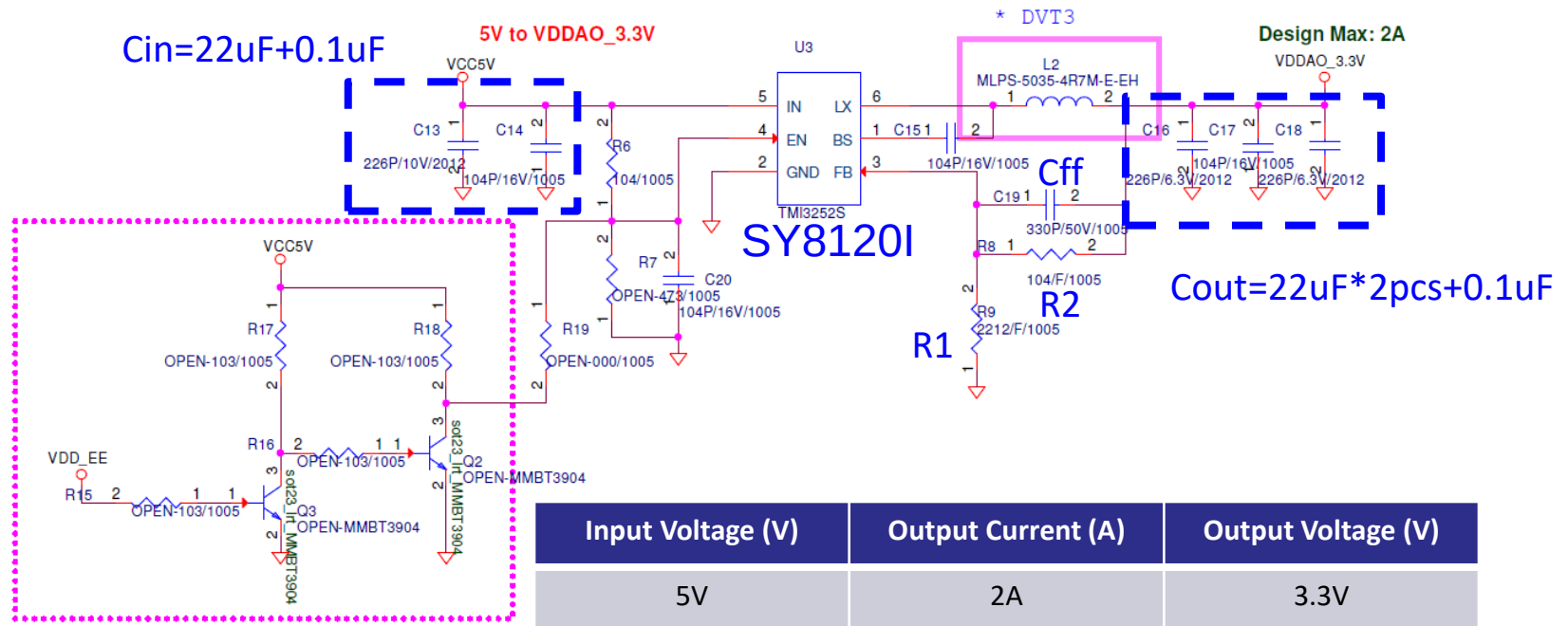


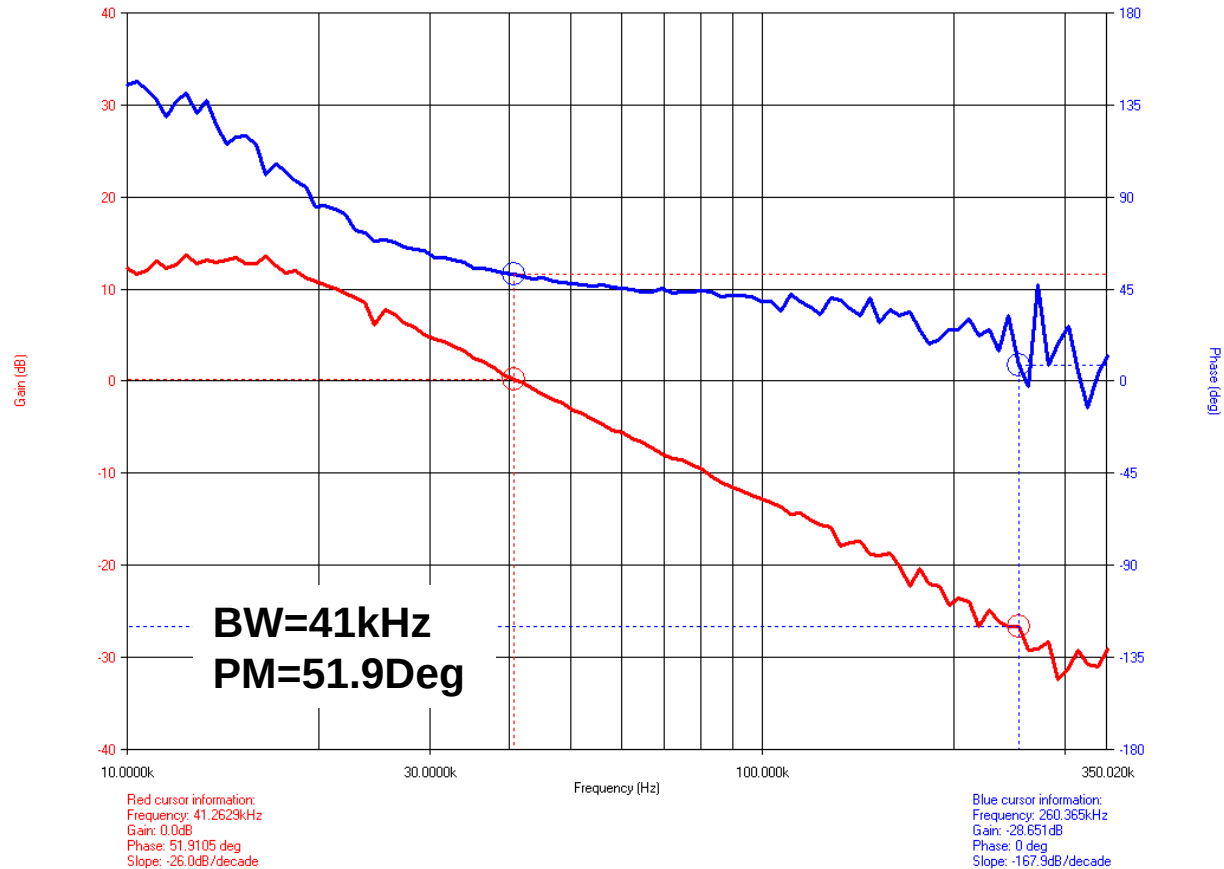
SY8120I Loop Gain Test

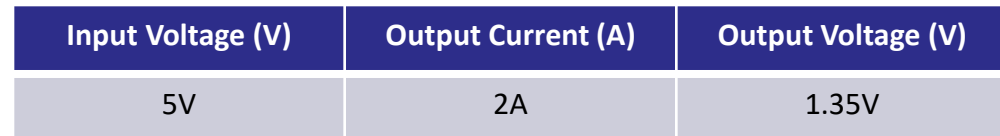
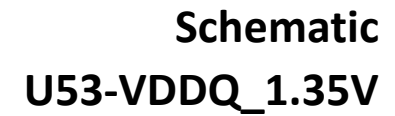
**Prepared by Silergy
2021.07.01**



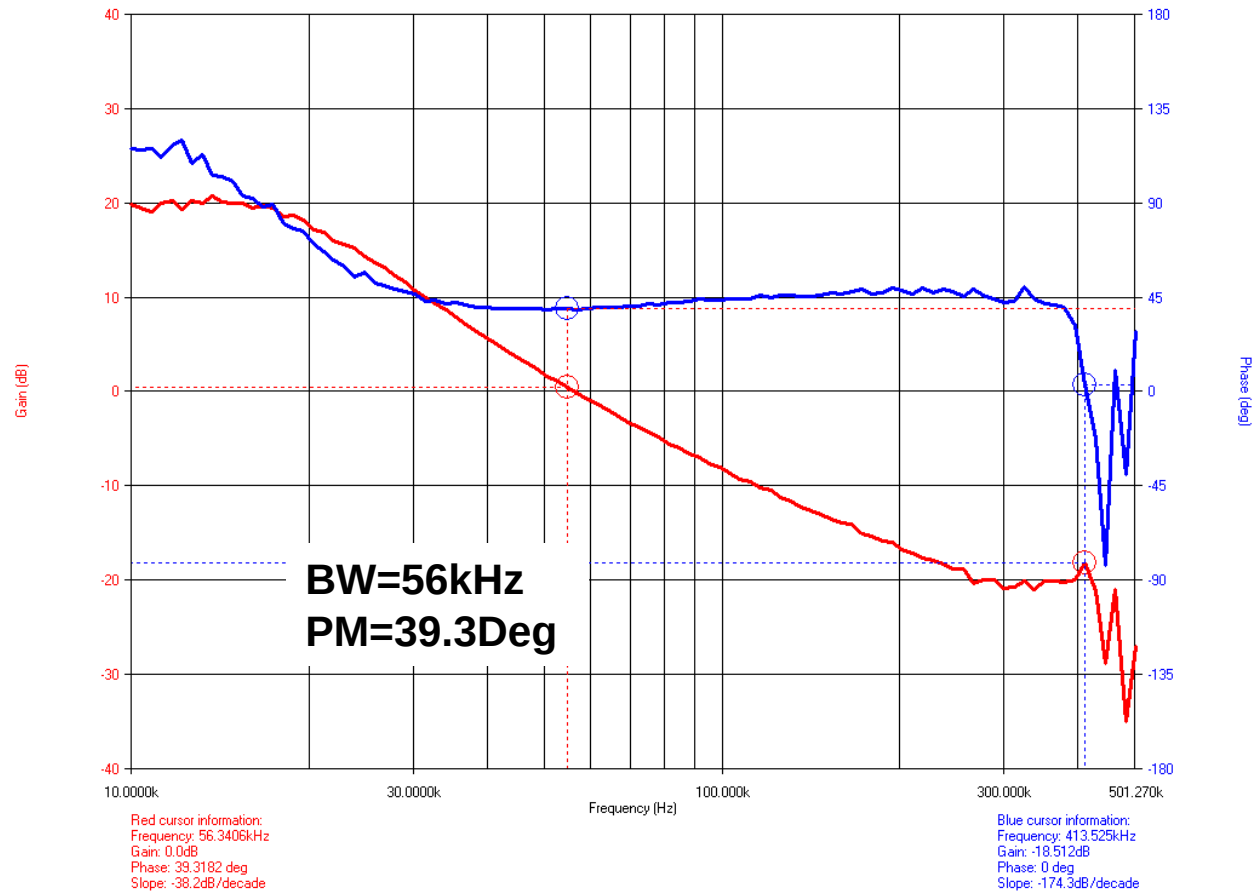
Customer BOM

V _{OUT} (V)	R ₂ (KΩ)	R ₁ (KΩ)	C _{ff} (pF)	C _{in} (μF)	C _{out} (μF)	L
3.3V	100	22.1	330	22uF+0.1uF	22uF*2pcs+0.1uF	4.7uH

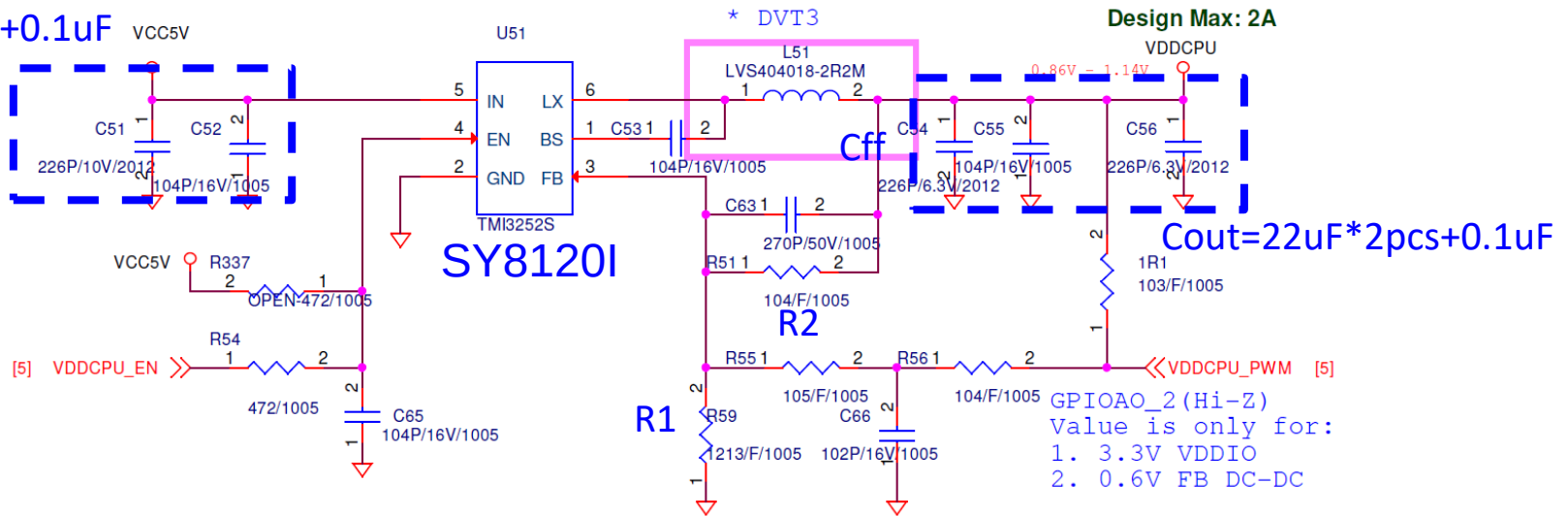




V _{OUT} (V)	R ₂ (KΩ)	R ₁ (KΩ)	Cff(pF)	Cin(μF)	Cout(μF)	L
1.35V	124	95.32	330	22uF+0.1uF	22uF*2pcs+0.1uF	3.3uH



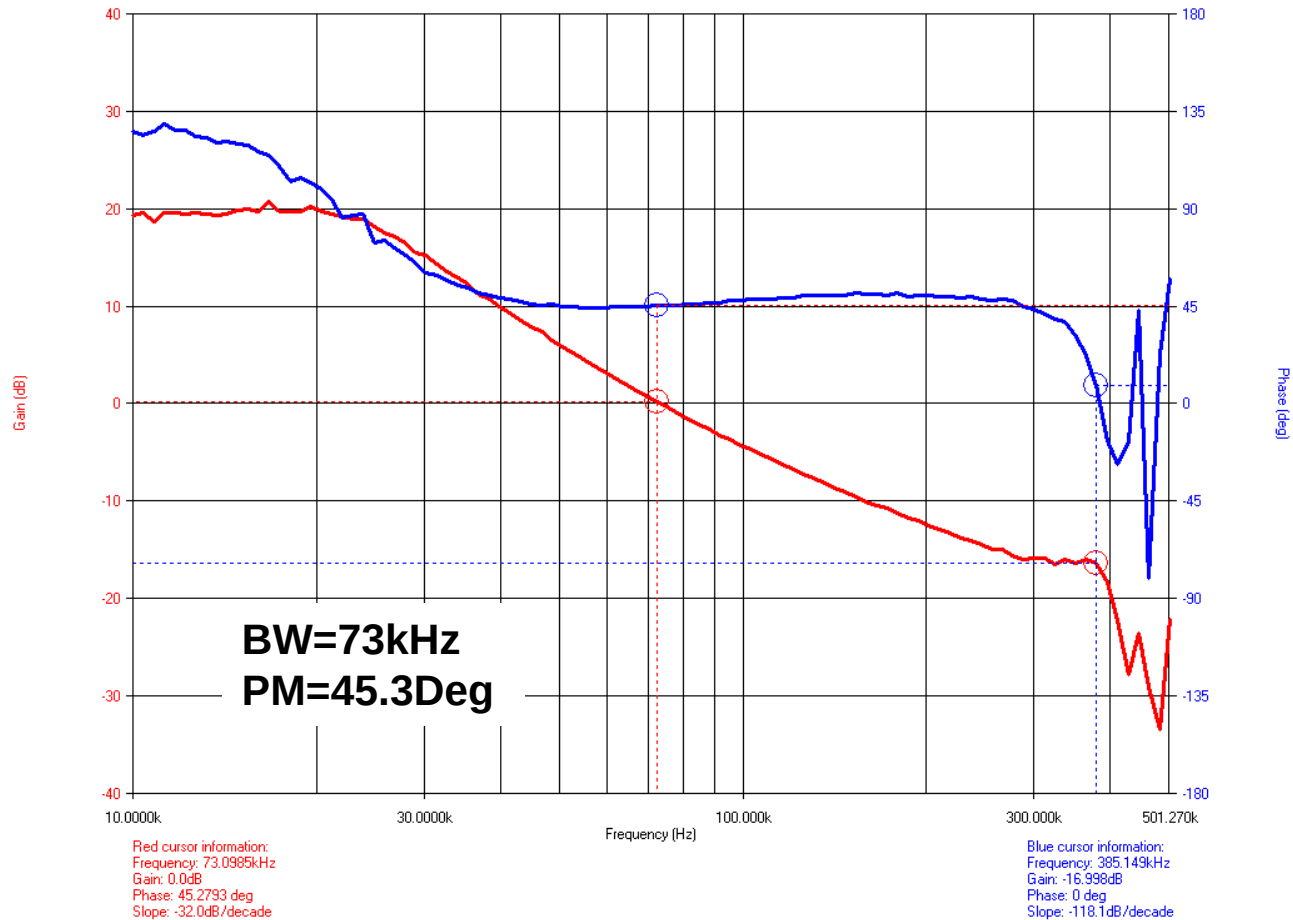
$C_{in}=22\mu F+0.1\mu F$

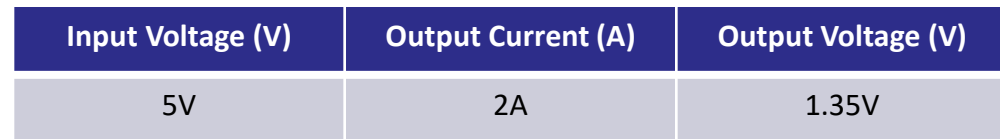
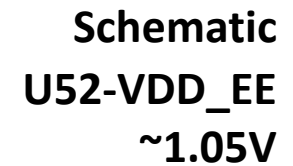


Input Voltage (V)	Output Current (A)	Output Voltage (V)
5V	2A	1.1V

Customer BOM

$V_{OUT}(V)$	$R_2(K\Omega)$	$R_1(K\Omega)$	$C_{ff}(pF)$	$C_{in}(\mu F)$	$C_{out}(\mu F)$	L
1.1V	100	121	270	22uF+0.1uF	22uF*2pcs+0.1uF	2.2uH





V _{OUT} (V)	R ₂ (KΩ)	R ₁ (KΩ)	Cff(pF)	Cin(μF)	Cout(μF)	L
1.05V	124	162	270	22uF+0.1uF	22uF*2pcs+0.1uF	2.2uH

